

IEEE802.11n/ac/ax Wi-Fi LDPC Decoder

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802.11n/ac/ax LDPC Decoder The 802.11n/ac/ax LDPC decoder is developed for high throughput WLAN applications.

Features

- Layered decoding
- Soft decision decoding
- IEEE 802.11n/ac/ax standard compliant
- Support all LDPC code rates ($\frac{1}{2}$, $\frac{2}{3}$, $\frac{3}{4}$, and $\frac{5}{6}$)
- Support all LDPC codeword sizes(648, 1296, and 1944 bits)

Benefits

- High throughput design
- Frame-to-frame on-the-fly configuration
- Configurable LDPC decoding iterations allows trading-off throughput and error correction performance.

Key Features

- Throughput matching the required specifications.
- Bit-error-rate and packet-error-rate performance meeting the required specifications

Applications

- Wireless Local Area Networks (WLAN)

Deliverables

- Synthesizable Verilog
- System Model (Matlab)
- Verilog Test Benches
- Documentation

A comprehensive IEEE802.11n/ac/ax Wi-Fi LDPC decoder datasheet can be provided under an NDA, please contact info@global-ipc.com.