

DVB-T2 Demodulator and LDPC/ BCH Decoder

Features

- DVB-T2 EN302 755 V1.2.1, Rev.9 compliant
- Supports IF input
- Single input Single output (SISO)
- Sampling frequency offset (SFO) tracking and compensation
- Carrier frequency offset (CFO) detection and correction
- Flexible channel BW (1.7, 5, 6, 7, 8, and 10) MHz
- BPSK, QPSK, QAM constellations 16, 64, and 256
- Channel BW (1.7, 5, 6, 7, 8, and 10) MHz
- Soft demodulation
- FFT size (1, 2, 4, 8, 16, and 32) K
- Guard interval (1/128, 1/32, 1/16, 19/256, 1/8, 19/128, and 1/4)cessor
- Coding rate (1/2, 3/5, 2/3, 3/4, 4/5, and 5/6)
- Short and long frames
- Layered Min-Sum LDPC decoder
- BCH decoder
- Parallel and Serial MPEG outputs
- 1 external clock
- SPI port (Slave) to external processor

Application Areas

- Set-top boxes
- Digital TV receivers

System Overview

The demodulator is designed to be used together with an RF tuner, and an analog to digital converter. The system has an internal state machine to control the operation, and can be externally configured via the SPI interface.

A comprehensive DVB-T2 Demodulator and LDPC/ BCH Decoder datasheet can be provided under an NDA, please contact <u>info@global-ipc.com</u>.

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